

FELLOWSHIP REGISTRATION FORM

Name: Mr./Ms./Dr./Prof. _____	Document #
Designation : _____ Department : _____	
Institution Name : _____	
Institution Address : _____	
City : _____	State : _____ Postal Code : _____
Phone : _____	Fax : _____ Email : _____

Participation Deposit

A participation deposit of Rs. 1,500/- (Rupees One Thousand Five Hundred Only) is payable with this registration. Payment is to be made by account payee Cheque drawn in favour of "VLSI TRUST 2005" payable at Kolkata. ***This amount will be refunded only to those who attend all 5 days (Conference and Tutorials).***

Cheque No.: _____ Dated : _____ Amount Rs. _____

Drawn on (Bank Name & Branch) : _____

(Please write your name and fellowship document number on the reverse of the cheque)

Tutorial Program

Please enter your order of preference next to the tutorial codes for each day (1, 2 or 3 for 1st, 2nd, 3rd preference respectively) :

Jan 6, 2005 (T1, T2, T3, T4 run in parallel)		Jan 7, 2005 (T5, T6, T7, T8 run in parallel)	
T1 []	Power-Aware reliable microprocessor design	T5 []	Good, Cheap, and Fast Testing of Digital Monster Chips
T2 []	High-Speed Interconnect Technology: On-Chip and Off-Chip	T6 []	Recent Advances in Verification, Equivalence Checking & SAT-Solvers
T3 []	A: Testing Nanometer Integrated Circuits: Myths, Reality and the Road Ahead B: Test Methodologies in the Deep Sub-micron Era - Analog and RF	T7 []	A: Compact MOSFET Models for Low Power CMOS Design B: Physics and Technology: Towards Low-Power DSM Design
T4 []	SoC Design Methodology: A Practical Approach	T8 []	Architectural, System Level and Protocol Level Techniques for Power Optimization for Networked Embedded Systems

Accommodation Required (tick [√] as appropriate) : Yes [] No []

Free accommodation can only be provided to a selected few on request. Please check the fellowship awardee list for your entitlement.

Completed Form and D/D must reach the Conference Secretariat at the following address by Dec. 13, 2004 :

VLSI 2005 CONFERENCE SECRETARIAT

C/o. Interra Systems India (Pvt.) Ltd.
2nd Floor, STP II Building, Salt Lake Electronics Complex
Plot No. 53, Block DN, Sector V, Bidhannagar, Kolkata 700 091, India.
Phone: (91) (33) 2367 3600, Fax: (91) (33) 2367 3520

For registration related queries please write to: vlsi05regn@vlsi-india.net
For any other query please write to the organizers: vlsi05org@vlsi-india.net

Date: _____

Signature _____

Sponsored by:



VLSI Society of India (VSI)



Indian Statistical Institute, Kolkata

Technically Co-sponsored by:

